



Mini-assembler

Small, but brave, short and powerful

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Are you annoyed time and again by having to calculate branch offsets for branch instructions? We are not anymore, because the “Mini-assembler” described here makes programming the KIM considerably easier in many respects. The program itself requires only 254 bytes of RAM/ROM and will therefore be a welcome aid to many KIM users when writing other programs.

Fig. 1 Keyboard layout for the Mini-assembler.

AD 10	DA 20	PC 30	+	40
C	D	E	F	
8	9	A	B	
4	5	6	7	
0	1	2	3	

Operation

What does this Mini-assembler do? Labels can be placed easily, without losing programming space. Once placed, they can be looked up easily later. The address and the corresponding label then appear on the display. Labels can be used with all branch instructions and all 3-byte instructions (thus also, for example, with LDA ABS).

This works as follows. Once you have entered the program, set the NMI vector to 0200 (the starting address of the Mini-assembler). To place a label, make sure that the address where the label is to be placed is shown on the display. A label name is a

combination of digits. The KIM keyboard is therefore used differently in the program, as shown in Fig. 1. A total of 80 decimal labels can consequently be used, represented by the numbers 00 through 4F hex. The way in which the labels are stored in memory is shown in Fig. 2.

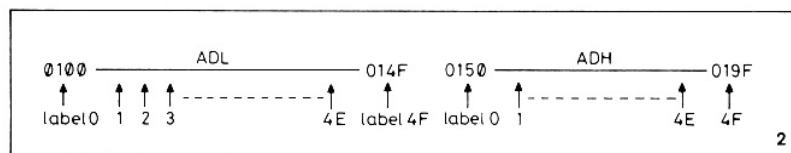


Fig. 2 Overview of how the labels are stored in memory.

To program labels 00 through 0F hex, press the “ST” key and then a key between 0 and F hex corresponding to the label you want to place (label 02 is: “ST”, “2”). When you release that key you are back in the monitor program and can continue entering the program. The labels are divided into groups: 00 through 0F is label group 0, 10 through 1F is label group 1, etc.

The example mentioned above therefore concerned label group 0. To program other label groups, proceed as follows.

Label group 1: “ST”, “AD” (short), “0” (through F).

Label group 2: “ST”, “DA” (short), “0” (through F).

Label group 3: “ST”, “PC” (short), “0” (through F).

Label group 4: “ST”, “+” (short), “0” (through F).

The “AD”, “DA”, “PC” and “+” keys therefore indicate the label group. These keys have another function, which will be discussed later. If you want to use these keys to indicate a label group, they must be pressed briefly. As with label group 0, after pressing a digit key you return to the monitor and can continue entering the program. If you continue holding the key down, the label number appears on the data display as a check. A second function is “find label”. If you suspect that you have already used a label, or no longer remember where a particular label is located, you can find it by pressing “ST”, then briefly “GO”, and then the label name. After these operations you are back in the monitor program and the display shows the address belonging to the label just searched for. If you now want to continue entering from the address where you left off, simply press the “PC” key. If you know the address where you placed a label but do not know which label it is, enter the relevant address while still in the monitor and then press “ST”. The address display remains at the address you entered. The data display shows FF if no label was placed at that address, and the label name if a label was placed there. If you then want to continue entering a program, to enter Data mode you must hold “DA” down until the label number shown on the data display is replaced by the actual data. To enter Address mode, do all of the above but use the “AD” key instead of “DA”. The same applies to “PC” and “+”. After this, everything starts again from the beginning. An overview of all functions can be found in Fig. 3.

Functie	Toetsen			
label plaatsen	[ST]	[AD] [DA] [PC] [+]	[0] : : : : [F]	
adres bij label	[ST] [GO]	[AD] [DA] [PC] [+]	[0] : : : : [F]	
label dat bij adres hoort	[AD] xxxx [ST] adres	[AD] * [PC] *		
uitvoeren	[ST]	[AD] [DA] [PC] [+]		[GO] *

* = lang indrukken (ongeveer 2 sec)

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Fig. 3 Overview of the functions to be used.

Running the program

Once you have entered the program being developed, including the labels, you can replace them per label group. For example, if you have used label group 0, put label 00 at the starting address and label 0F at the ending address (“AD”, start address, “ST”, 0, “AD”, end address, “ST”, F). Now enter the starting address again after “AD” and then press “ST”. Next, hold the “GO” key down until the end address appears on the address display and the starting label appears on the data display. When this has happened, all branch instructions and all 3-byte instructions should point to the correct addresses.

Indicating labels in a program

All relative branch instructions end in 10000 binary. When you do not want to use a label, enter a relative branch in the normal way, for example:

20 1F 1F

E6 00

DO F9

F9 is now not a label, but an actual offset. If you want to refer to labels, change X0 to X2.

The second byte of the (now no longer existing) instruction gives the label number. In the example this becomes:

“AD”, 0300,

“ST”, 1 (label 01)

20 1F 1F

E6 00

D2 01 (label 01)

Labels can also be used with 3-byte instructions. The first byte remains unchanged. The second byte is replaced by the label number and the third byte is always F2, provided that page F2 is not used for RAM/ROM. In our example there was a 3-byte instruction. We will replace this with a 3-byte instruction using a label. Address 1F1F hex must receive label 02; the rest remains the same.

“AD”, 1F1F, “ST”, 2,
“AD”, 0300, “ST”, 1,
20 02 F2
E6 00
D2 01

To illustrate what has been discussed, the program “Checksum” from Listing 1 is entered step by step using labels. Label group 1 is used.

“AD”, 0300, “ST”, “AD”, 0:
label l0 at starting address

“DA”, A2 + FE +
86 + F6 +
86 + F7 +

“ST” “AD” 1:
label 11 at address 0306

BD + 12 + F2 +:
label 12 at address 01FF

20 + 13 + F2 +:
label 13 at address 1F91

CA +
D2 + 11 +
4C + 14 + F2 +:

label 14 at address 1C22

Now label 12 must be placed at address 01FF, label 13 at 1F91, and label 14 at address 1C22.

“AD”, 01FF, “ST”, “AD”, 2
“AD”, 1F91, “ST”, “AD”, 3
“AD”, 1C22, “ST”, “AD”, 4

Now all labels have been placed and only the end and start addresses have to be specified.

“AD”, 0312, “ST”, “AD”, F
“AD”, 0300, “ST”, “AD”, 0

The labels can now be replaced. Press “ST” and then “AD”, because label group 1 must be converted. Next, hold the “GO” key down until the end address appears. If everything has gone correctly, all labels have been replaced and the program works. After running this program, address 00F6 should contain 81 hex and address 00F7 should contain F7 hex.

The program “Checksum” is a kind of addition program that adds together all bytes of the “Mini-assembler” program and places the sum at addresses 00F6 and 00F7 hex. It is therefore a check that the “Mini-assembler” has been entered correctly. This little program uses a routine from the KIM monitor, so it does not work directly on other systems, but the same also applies to the “Mini-assembler” itself.

Multiple label groups

If a program contains more labels than one label group can hold, all start labels (indicating the starting address) and all end labels (indicating the ending address) must be the same in order to convert all label groups at once. If you keep the label groups separate for the various parts of a program (for example, for subroutines), you only need to replace one label group, leaving the other groups untouched.

It is possible to place multiple labels at one address. When searching for a particular label, the highest label is always shown on the data display. One advantage of this program is that it is not always necessary to refer to labels. Those people who have made calculating branch offsets their hobby can therefore continue doing so. When you want to develop a new program, you can use the program from Listing 1 (see Part 1) to erase the labels.

Relative branches

X2 is used instead of X0 for relative branches. This was done first of all to distinguish it from X0, but also to prevent a program from going wrong if you forget to convert the labels. After all, the microprocessor does not recognize the instruction XXX10010 and will therefore stop. Only a reset can get the microprocessor running again. The 3-byte instructions end in F2. With branch instructions, the microprocessor therefore jumps to page F2. If no memory is present there, it encounters instruction F2, which also satisfies the bit pattern XXX10010.

Program

When the “ST” key is pressed, an NMI is generated. The microprocessor therefore jumps from the monitor to 0200 hex, where the stack pointer is set correctly and the label corresponding to the address visible on the address display is searched for (see Fig. 4). The timer for the “GO” key is set to 0, as is the memory location “High”, which is used to remember the label group. As long as no key is pressed, “Time” remains at zero. When “AD”, “DA”, “PC” or “+” is pressed, “Time” starts running and the corresponding label group is placed in “High”. If the key is held down for a long time (“Time” becomes zero again), the program returns to the monitor. The corresponding code is now interpreted and executed as a monitor function. If a digit is pressed, its value is added to “High”, which thus forms the final label. If the program is in search mode, the address shown on the display is placed in the monitor’s PC memory (00EF, 00F0 hex). The address belonging to the label just entered is placed on the display. As soon as the last key is released, the program returns to the monitor. If the program is not in search mode, the address belonging to the label is placed in the table. When the “GO” key is pressed, “Search” is incremented and, as long as “Search” is not 00 again, the program returns to the keyboard routine. If the “GO” key is pressed only briefly, “Search” will not be zero and the program is in search mode. If the “GO” key is held down for a long time, then at some point (after approximately 1 s) “Search” becomes zero and the program begins replacing the labels. The address of the starting label is placed in the pointer. The

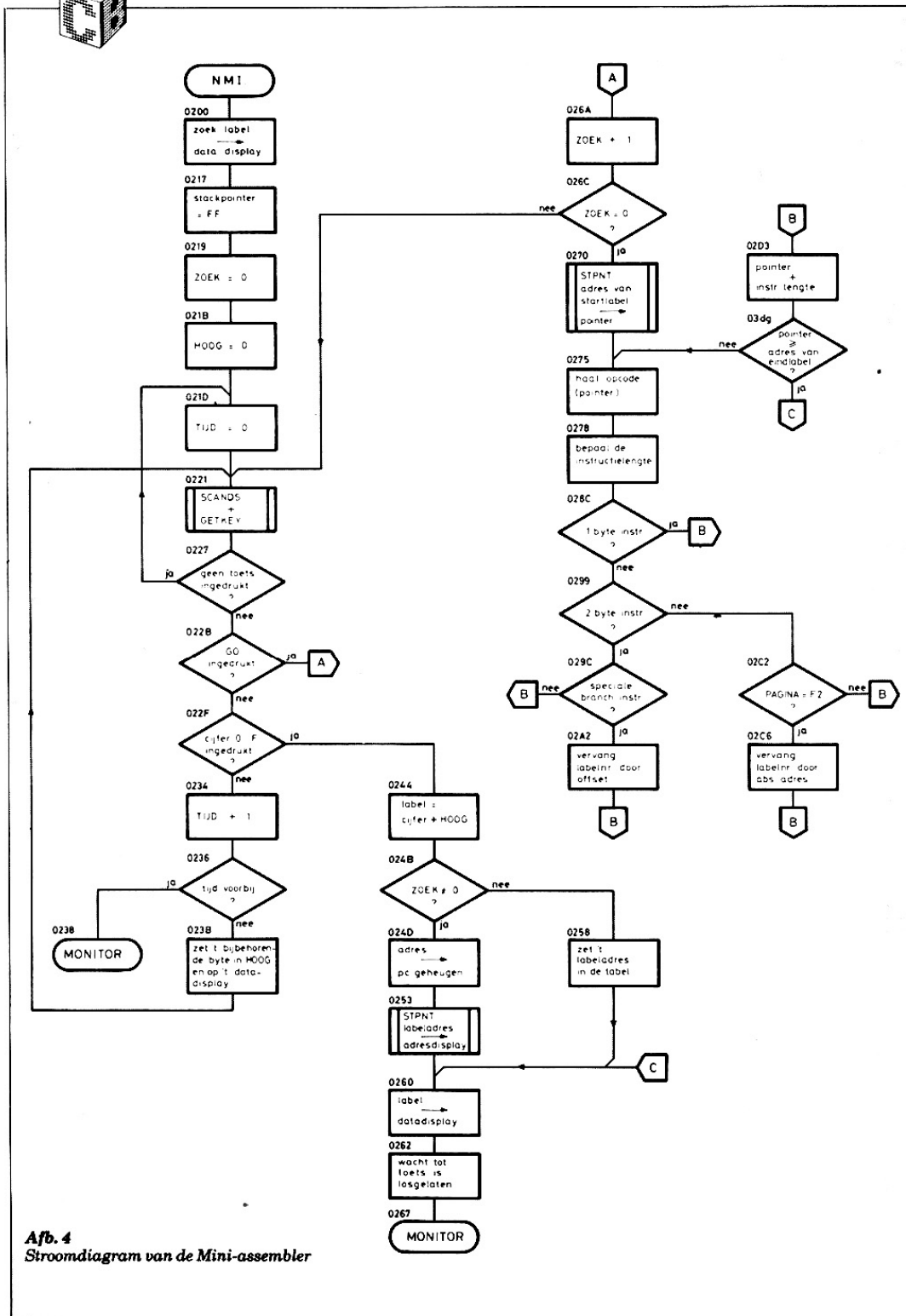
opcode of the first instruction is then fetched. The instruction length is subsequently determined. One-byte instructions are skipped. With 2-byte instructions it looks for the following bit pattern: XXX10010. If the instruction satisfies this, the last four bits are made 0 and the second byte of the instruction is replaced by the offset. With 3-byte instructions it first looks at the last byte. If this is F2, it replaces the last two bytes with the absolute address. The pointer is then advanced to the next instruction. If the pointer passes the end address, the program returns to the monitor as soon as the "GO" key is released. Finally, a few tips.

Now that you can work with labels, you will probably also want to remove or insert instructions. This is very easy if all relative branch instructions and all 3-byte instructions have been replaced by labels. You then use a "normal" shifting program and, when shifting, briefly check whether the address concerned represents a label (page 01 in KIM RAM). If it contains a label, the address of the label must be replaced by the new address.

It is often useful to put labels on cassette. This can be done by saving the memory block from address 0100 through 019F hex.



Mini-assembler



Afb. 4
Stroomdiagram van de Mini-assembler


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1180: 0300                                ORG    #0300
1190:
1200:
1210: 0300                                CHKHI  *    #00FC
1220: 0300                                CHKSUM *    #00F7
1230: 0300                                CHECK  *    #01FF
1240: 0300                                MONIT  *    #1C22
1250: 0300                                CHK    *    #1F91
1260:
1270:
1280:                                PROGRAMMA OM LABELS
1290:                                TE WISSEN
1300:
1310: 0300 A0 FF        LDWIM  #FF
1320: 0302 AC 00        LOXIM #00
1330: 0304 48          LOPU   PHX
1340: 0305 CA          DEY:
1350: 0306 D8 FC        BNE   LOOPU
1360: 0308 CA 22 1C     JMP   MONIT
1370:
1380:
1390: 0300                                ORG    #0300
1400:
1410:                                VOORBEELDPROGRAMMA OM DE
1420:                                CHECKSUM TE BEREKENEN VAN
1430:                                DE MINI-ASSEMBLER
1440:
1450: 0300 A2 FE        CSUM  LDXIM #FE
1460: 0302 B6 F6        STX   CHKHI
1470: 0304 86 F7        STX   CHKSUM
1480: 0306 D0 FF 01     LOOPF LDWIM  CHECK
1490: 0309 20 91 1F     JSR    CHK
1500: 030B CA          DEY:
1510: 030D D0 F7        BNE   LOOPF
1520: 030F CA 22 1C     JMP   MONIT

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